QUAD 2 INPUT HCC/HCF 4011B
DUAL 4 INPUT HCC/HCF 4012B
TRIPLE 3 INPUT HCC/HCF 4023B

- PROPAGATION DELAY TIME = 60 ns (typ.) AT
  \( C_L = 50 \text{pF}, V_{DD} = 10 \text{V} \)
- BUFFERED INPUTS AND OUTPUTS
- QUIESCENT CURRENT SPECIFIED TO 20 \text{mA}
  FOR HCC DEVICE
- INPUT CURRENT OF 100 \text{nA} AT 18 \text{V} AND 25 \text{°C}
  FOR HCC DEVICE
- 100% TESTED FOR QUIESCENT CURRENT
- 5V, 10V AND 15V PARAMETRIC RATINGS
- MEETS ALL REQUIREMENTS OF JEDEC TANTIVE STANDARD No. 13A, "STANDARD
  SPECIFICATIONS FOR DESCRIPTION OF "B" SERIES CMOS DEVICES"

DESCRIPTION
The HCC4011B, HCC4012B and HCC4023B (extended temperature range) and HCF4011B,
HCF4012B and HCF4023B (intermediate temperature range) are monolithic, integrated circuit, available in 14-lead dual in-line plastic or ceramic package and plastic micropackage.

ORDER CODES:
HCC40XXBF  HCF40XXBM1
HCC40XXBEY  HCF40XXBC1

The HCC/HCF4011B, HCC/HCF4012B and
HCC/HCF4023B NAND gates provide the system designer with direct implementation of the NAND
function and supplement the existing family of
COS/MOS gates. All inputs and outputs are bucketed.

PIN CONNECTIONS